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STATEMENT BY APPLICANT**
(Not for submission under 37 CFR 1.99)

Application Number	09802509
Filing Date	2001-03-09
First Named Inventor	Victor Keith Blanco
Art Unit	3714
Examiner Name	John M. Hotaling
Attorney Docket Number	MS1-0762US

U.S. PATENTS

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	1	5872915		1999-02-16	Dykes, ; et al.	
	2	7218739	B2	2007-05-15	Multerer, ; et al.	

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	1	JP10260939	JP		1998-09-29	FUJITSU LTD		<input type="checkbox"/>
	2	JP2000107424	JP		2000-04-18	SUN ELECTRONICS		<input type="checkbox"/>

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	1	Kasahara, "Follow-up Report! Operational Verification of Pentium III-compatible Applicationd & Compatible Chip Mother," DOS/V Magazine, Softbank Publishing Inc., May 1, 1999, Vol 8, No.9, pp 130-137.	<input type="checkbox"/>
	2	Official Notice of Rejection for Japanese patent application no. 2006-172872 mailed on September 14, 2007, 8 pgs.	<input type="checkbox"/>

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